



Docket No.: M4065.0767/P767
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Barmak Mansoorian

Allowed: May 26, 2006

Application No.: 09/359,056

Confirmation No.: 2286

Filed: July 21, 1999

Art Unit: 2615

For: HIGH RESOLUTION CMOS CIRCUIT
USING A MATCHED IMPEDANCE
OUTPUT TRANSMISSION LINE

Examiner: N. T. Tran

REQUEST FOR CORRECTION OF PTO RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In reviewing the above-captioned U.S. patent application upon allowance, the undersigned has noticed that the Attorney/Docket No. as reflected on the Notice of Allowance and Fee(s) Due has an error. The Attorney/Docket No. should read "M4065.0767/P767."

The PTO is therefore kindly requested to correct its records to reflect the correct docket number.

Applicants additionally request that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Application No.: 09/359,056

Docket No.: M4065.0767/P767

Dated: July 6, 2006

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO LLP

1825 Eye Street NW

Washington, DC 20006

(202) 420-2200

Attorney for Applicant